

ZZZ



0605 Change P/N for ZEJ00

PCB DAZ@
DA60012D000
PCB 125 LA-A791P REV0 M/B 3

ZZZ1



HDMI 45@
RC00000003HM

S1



FRAME
ECOMV000200

@

S4



FRAME
ECOMV000200
for GLONASS

@

Compal Confidential

Schematics Document

ZEJ00
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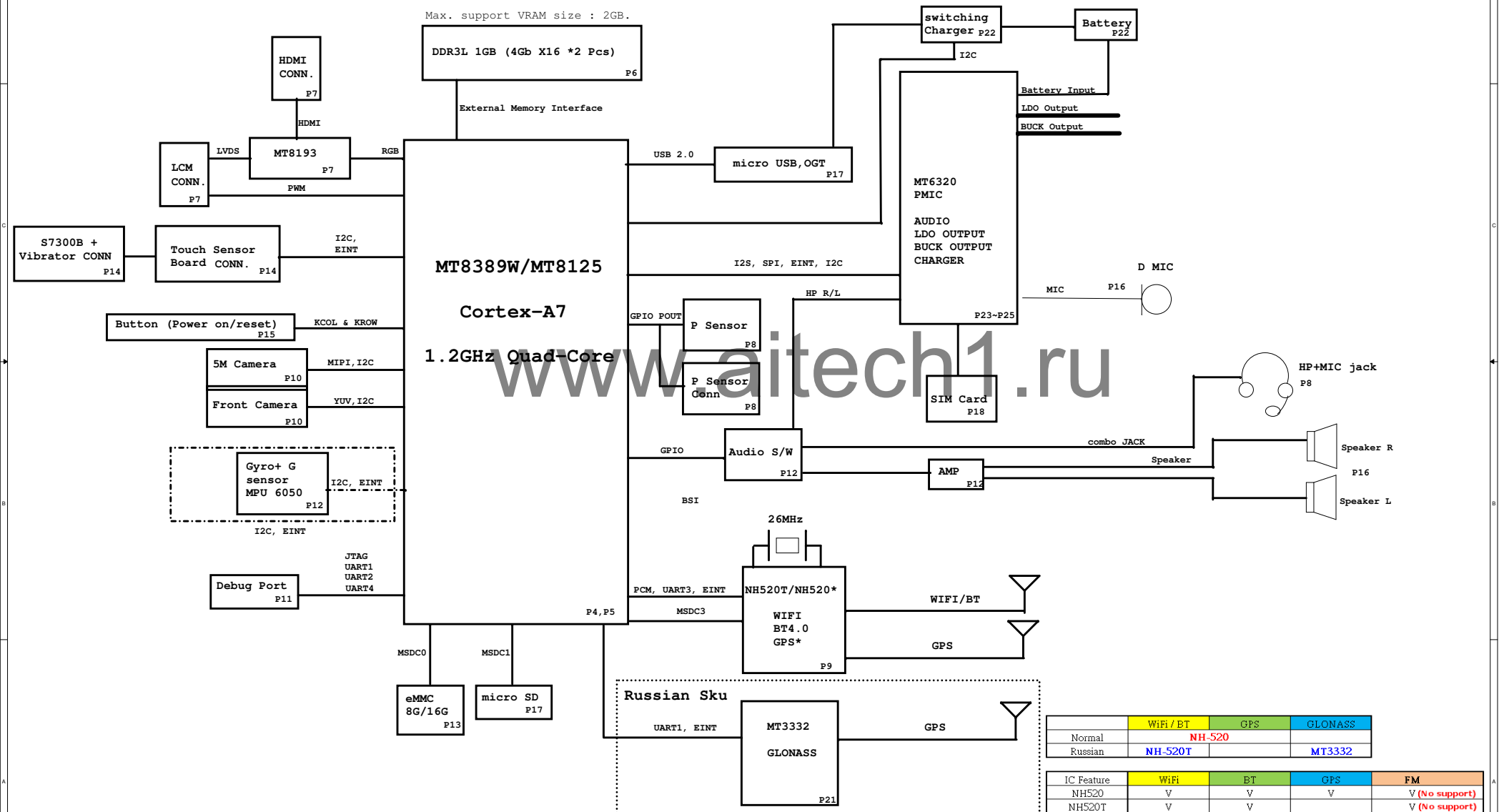
LA-A791P

2013-05-27

REV: 0.2

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				Date: Thursday, August 08, 2013	Sheet 1 of 28

ZEJ00 block diagram



Voltage Rails

MT6320 Power Plane	Function	Power Level	IDLE	Sleep mode
VCORE_PMU	VCORE switching output	0.75 ~ 1.3	ON	Low voltage
VPROC_PMU	VPROC switching output	0.75 ~ 1.3	ON	Low voltage
VTCXO_1_PMU	LDO output for TCXO	2.8V	ON	OFF
VCAMA_PMU	LDO output for camaera analog	2.8V	ON	Gating by SW
VSRAM_PMU	LDO output used for 1.2V SRAM	1.2V	ON	Low voltage
VDD28_6583	LDO output used for 2.8V IO	2.8V	ON	Gating by SW
VGP2_PMU	LDO output for camaera 1.8V	1.8V	ON	Gating by SW
VEMC_3V3_PMU	LDO output for eMMC&P-sensor	3.3V	ON	Gating by SW
VMCH_PMU	LDO output for SD card	3.3V	ON	Gating by SW
VGP5_PMU	LDO output for Touch panel	2.8V	ON	Gating by SW
DDR3VCCIO	LDO output for DDR3L	1.35V	ON	Gating by SW
				Gating by SW
VDD18_6583	LDO output used for 1.8V IO	1.8V	ON	Gating by SW
VRF18_PMU	LDO output for RF_MT6167	1.8V	ON	Gating by SW

I2C address

Device	Address(7 bit)	Address(8bit)	
		Write	Read
Gyro (MPU-6050)	0x68	0xD0	0xD1
G-sensor (MPU-6050)	0x68	0xD0	0xD1
Touch screen (S7300B)	0x20	0x40	0x41
Camera 0.3M	0x21	0x42	0x43
Camera 5M	0x36	0x6C	0x6D
PMIC			
Charger	0x6B	0xD6	0xD7
Battery	0x55	0xAA	0xAB

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Main board ID

MB_ID0	MB_ID1	
0	0	EVT
0	1	DVT
1	0	PVT
1	1	MP

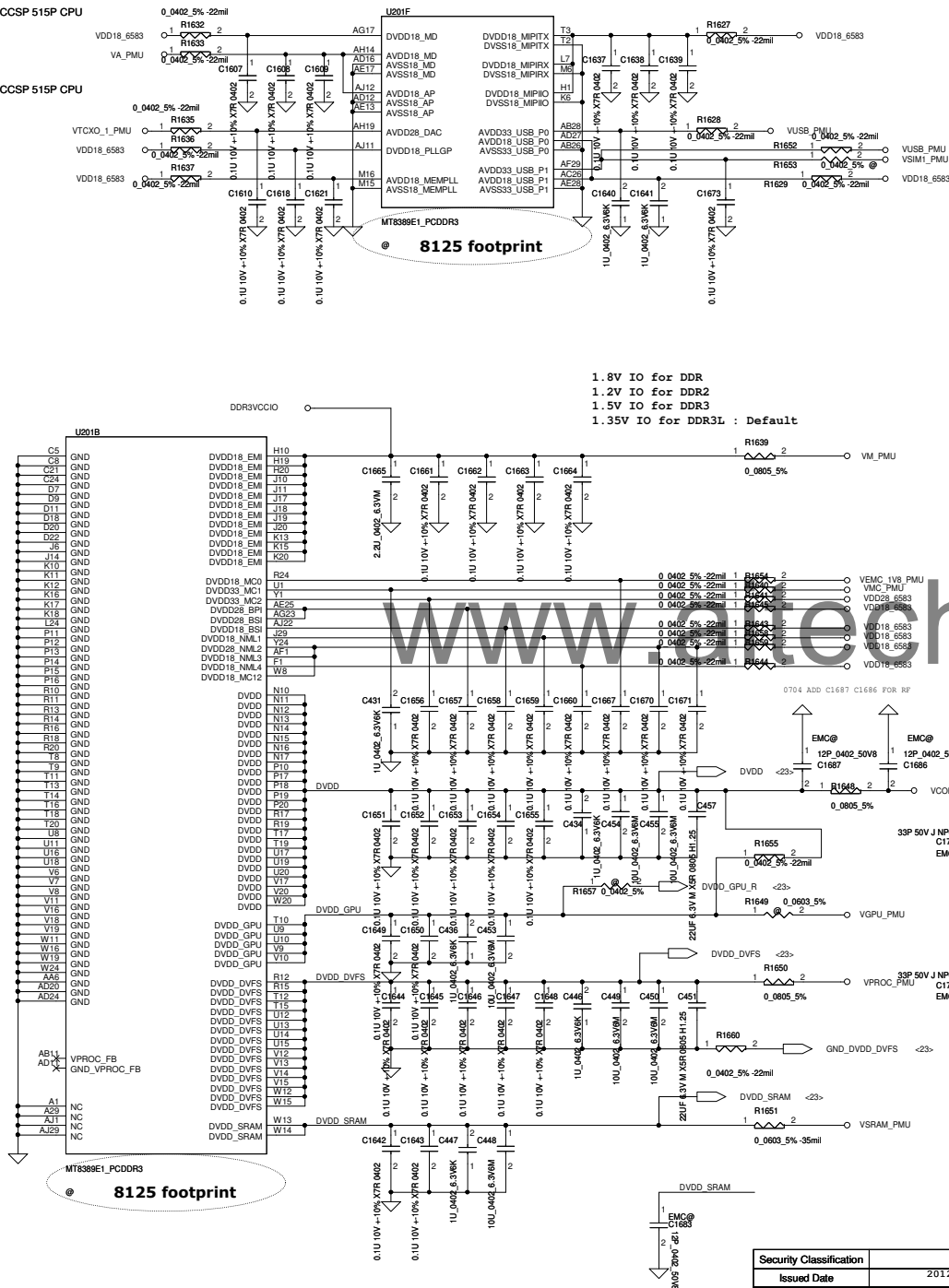
BOM structure

Name	Function	
3G@	3G only	
WIFI_ONLY@	WIFI ONLY	
DAZ@	PCB	
EMC@	for EMC request	
NH520@	AW-NH520	
NH520_EMC@	GPS EMC	
GLONASS@	MT3332	

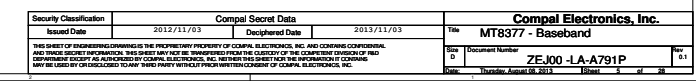
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				Date: Thursday, August 08, 2013	Sheet 3 of 28

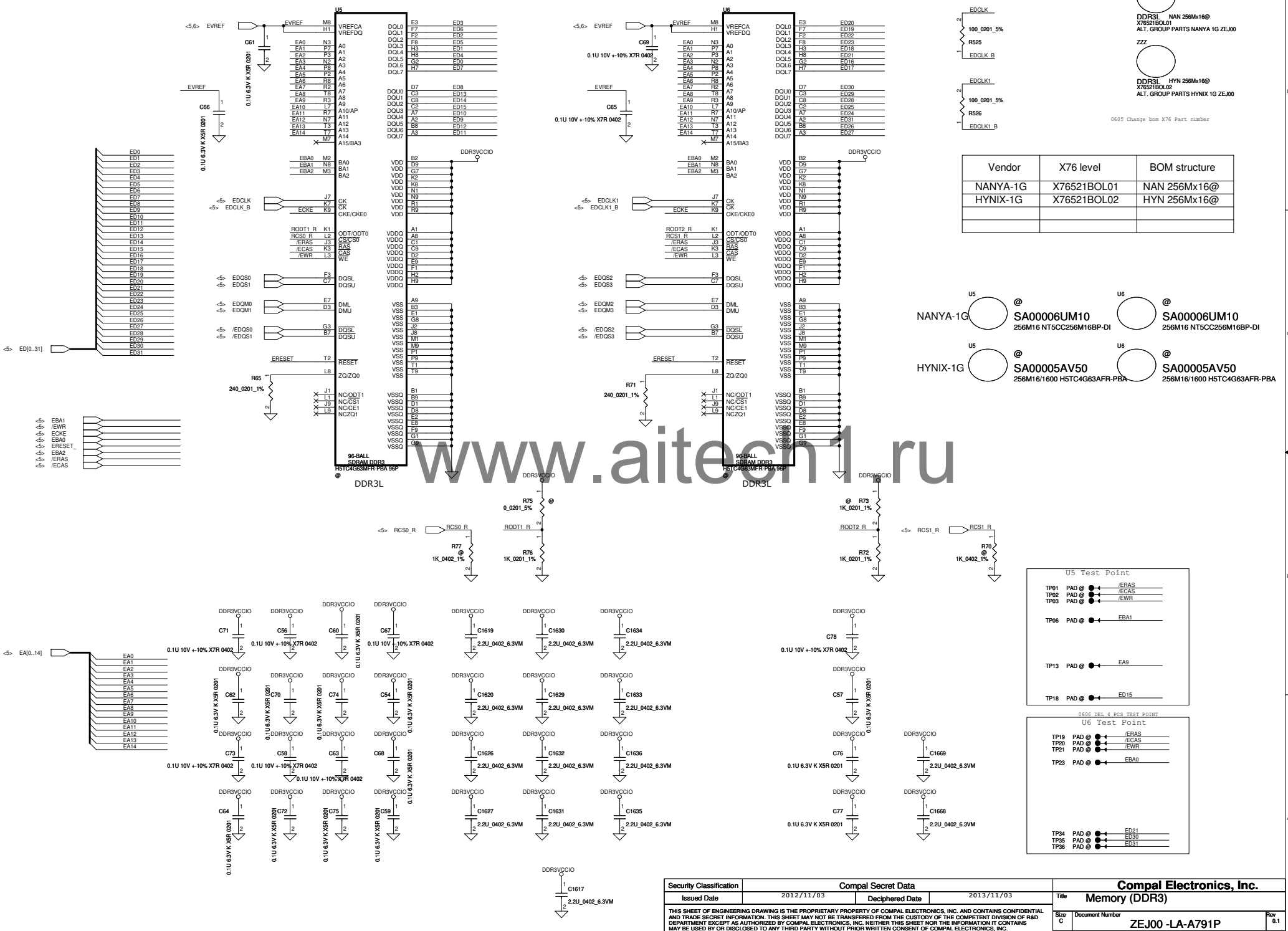
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3G@
SA00006SG00
S IC MT8389WK/A 1.2G FCCSP 515P CPU

U201
WIFI_ONLY@
SA00006S510



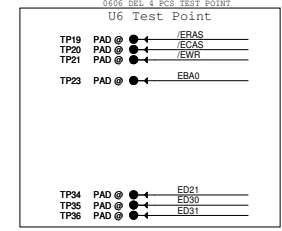
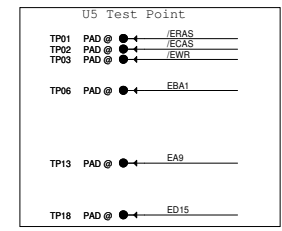
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				Date:		Thursday, August 06, 2013		Sheet 4 of 28	





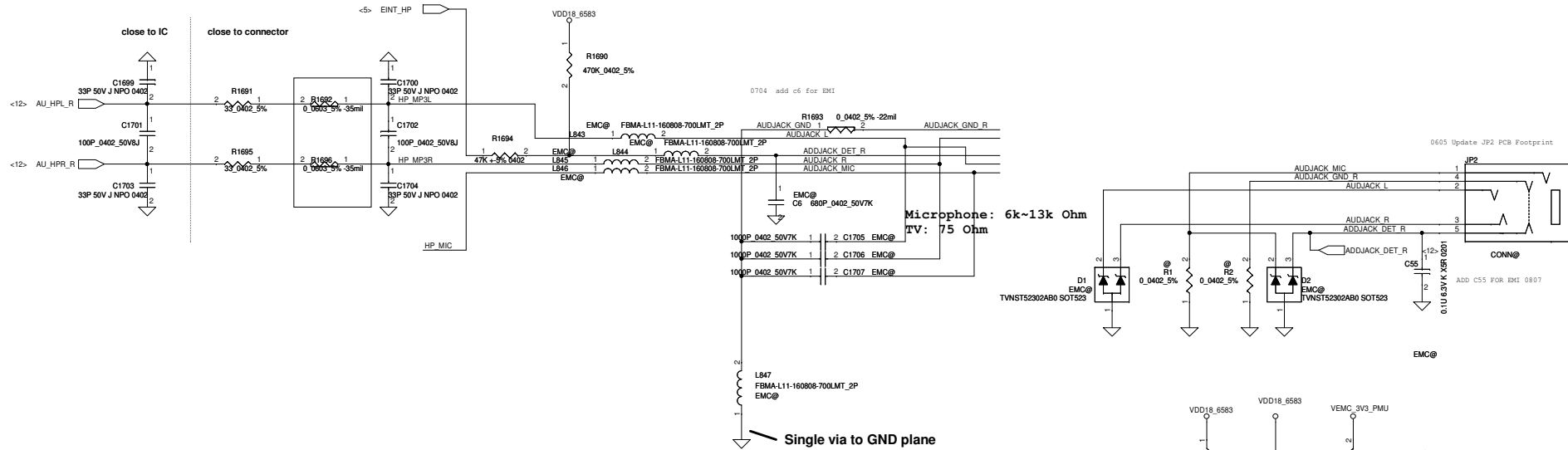
Vendor	X76 level	BOM structure
NANYA-1G	X76521BOL01	NAN 256Mx16@
HYNIX-1G	X76521BOL02	HYN 256Mx16@

NANYA-1G	@ SA00006UM10 256M16 NT5CC256M16BP-DI	@ SA00006UM10 256M16 NT5CC256M16BP-DI
HYNIX-1G	@ SA00005AV50 256M16/1600 H5TC4G63AFR-PBA	@ SA00005AV50 256M16/1600 H5TC4G63AFR-PBA

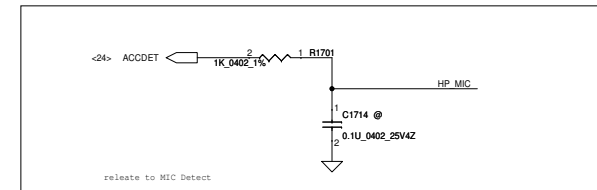
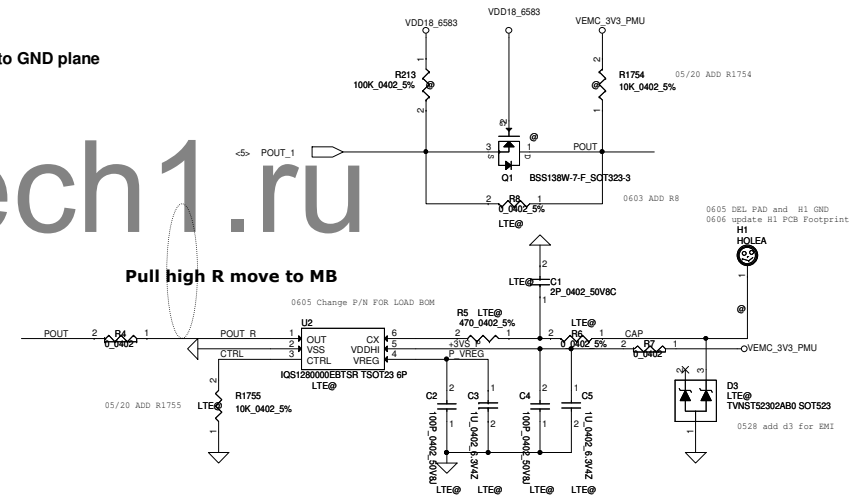
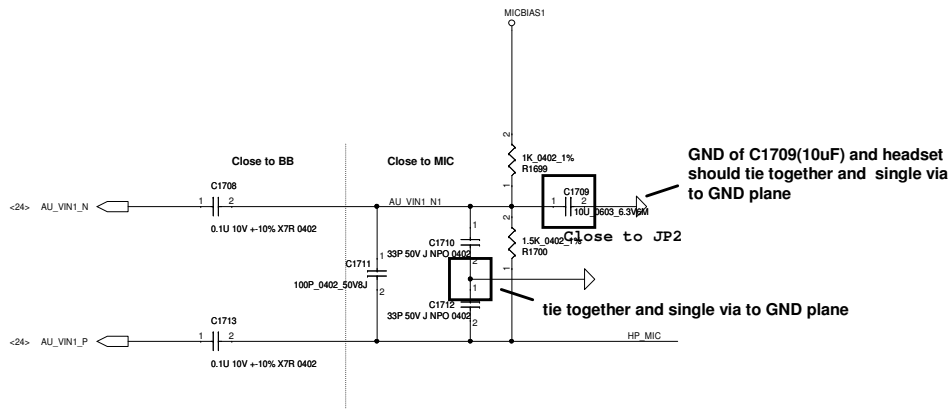




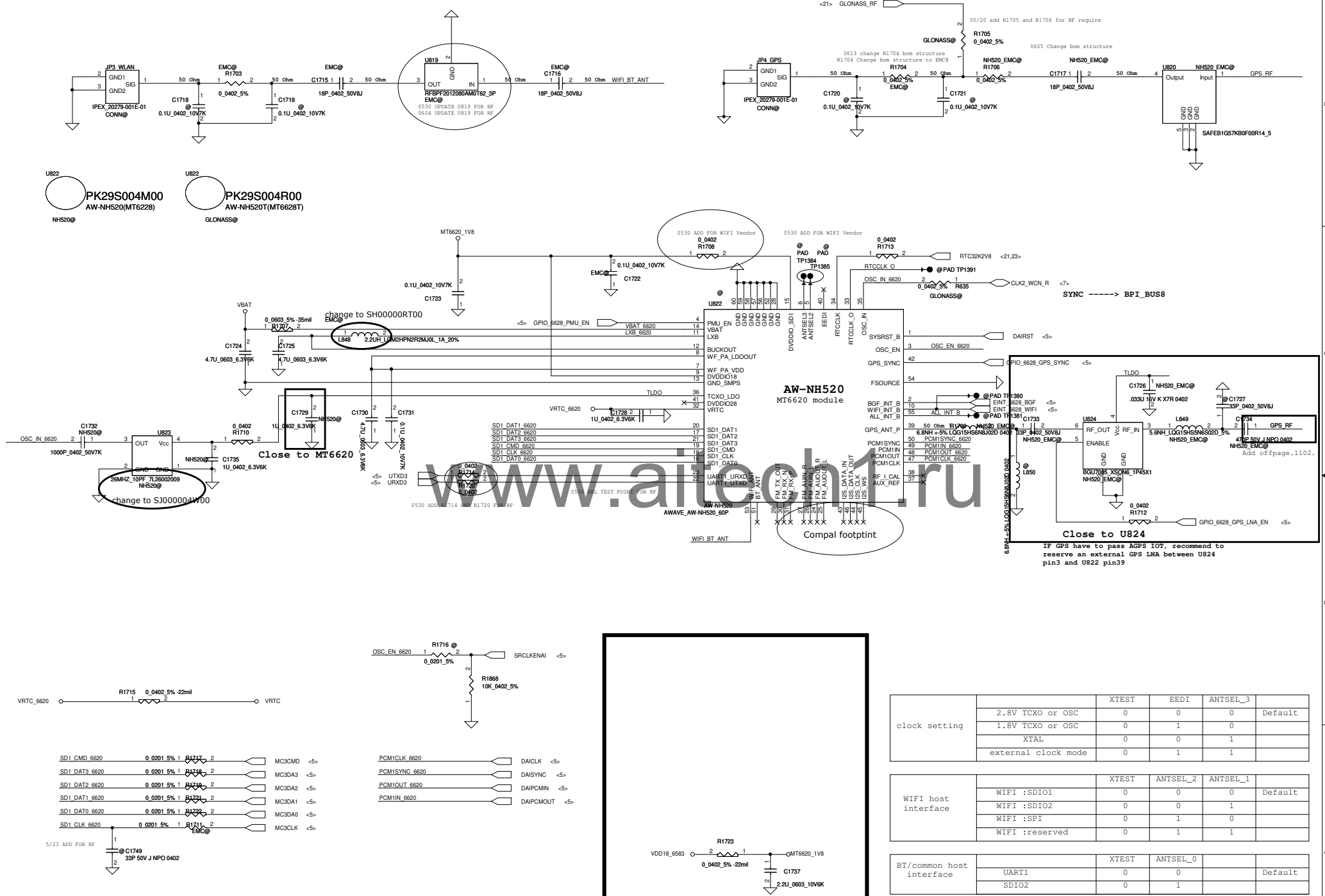
Earphone RECEIVER



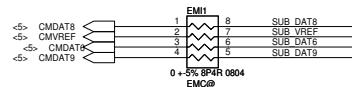
Earphone MICPHONE



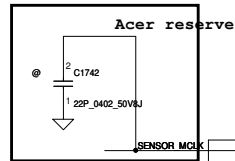
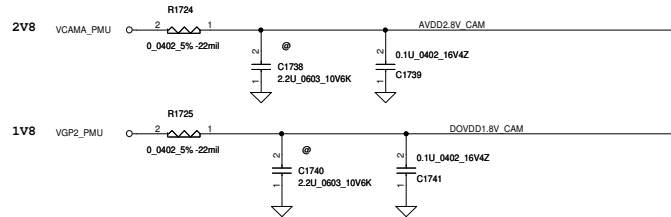
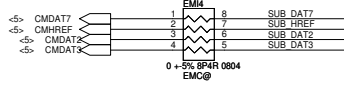
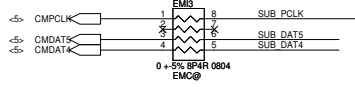
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Size	C	Document Number	ZEJ00 -LA-A791P	Rev
Date	Thursday, August 08, 2013	Sheet	8	of 28



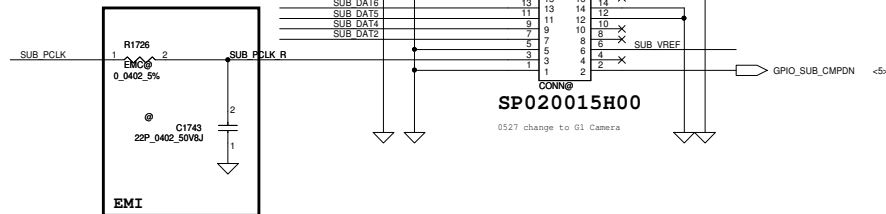
Sub Camera



05/20 DEL EM12

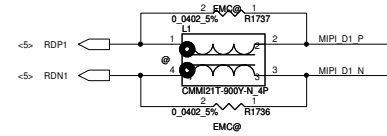


0.3M connector

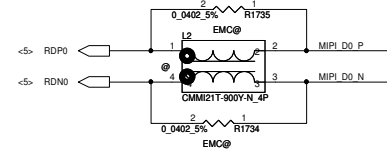
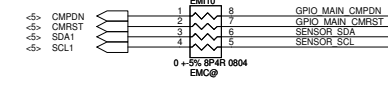


Sub Camera

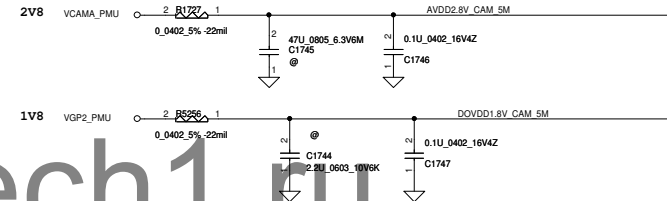
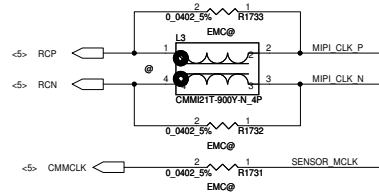
0531 Change L1 L2 L3 R1731 FOR MTK



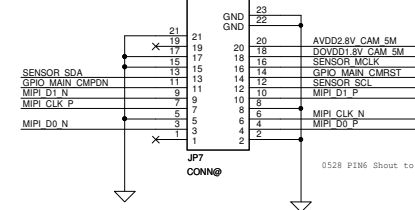
05/20 SWAP FOR Layout



05/20 Change C1745 from 0603 to 0805

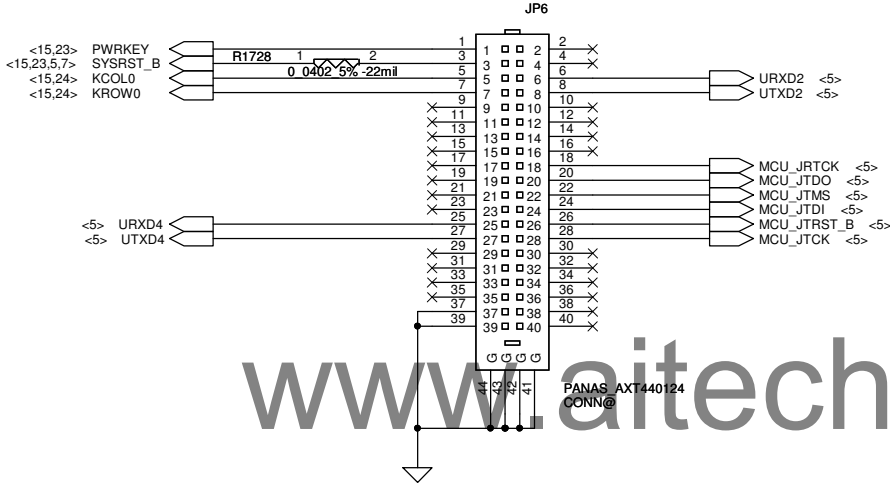


0521 Change P1 for ME



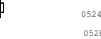
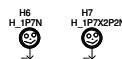
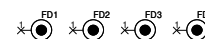
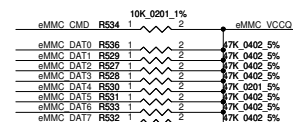
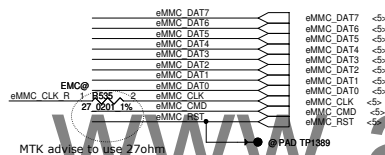
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Size	Document Number	ZEJ00 -LA-A791P		Rev
C				0.1
Date:	Thursday, August 08, 2013	Sheet	10	of 23

Common Debug

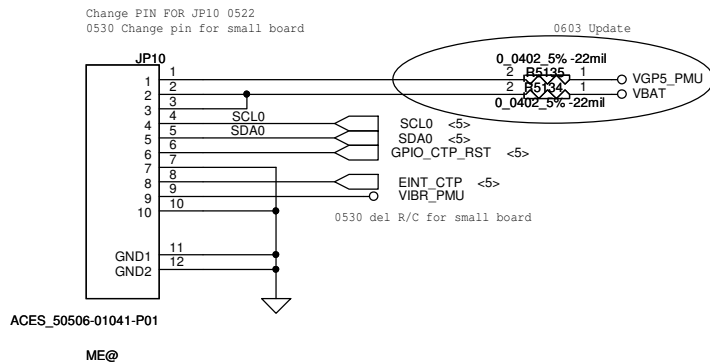


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				Date:	Thursday, August 08, 2013
				Sheet	11 of 28

Close to Memory



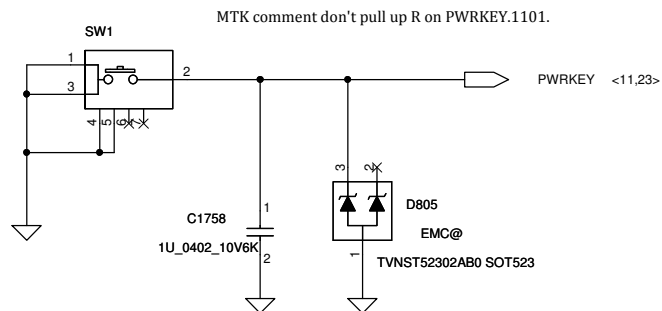
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				Size C	Document Number	Rev
				ZEJ00-LA-A791P		0.1
Date:	Thursday, August 08, 2013		ISheet	13	of	28



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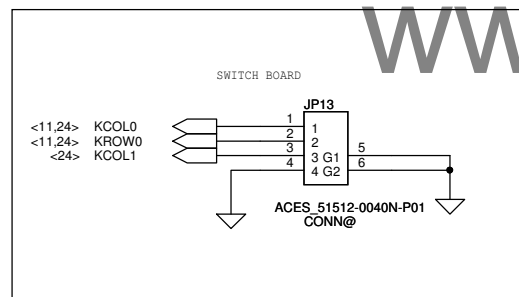
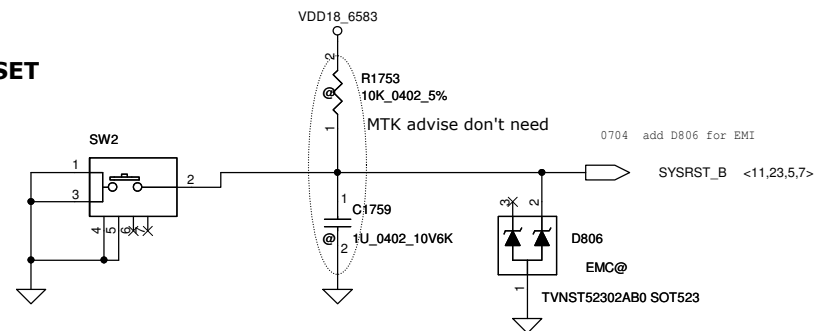
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Size B	Document Number ZEJ00 -LA-A791P		Date: Thursday, August 08, 2013	Sheet 14 of 28

Power-on Button



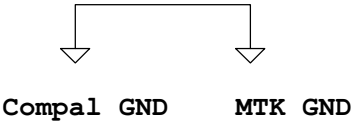
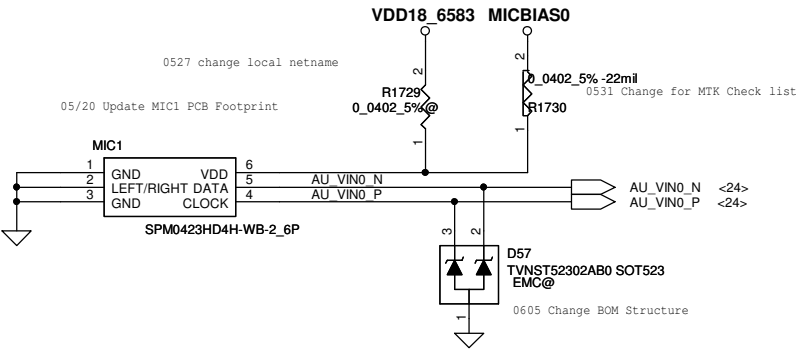
Compal GND MTK GND

RESET



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				Document Number ZEJ00-LA-A791P
				Rev 0.1
				Date: Thursday, August 08, 2013
				Sheet 15 of 28

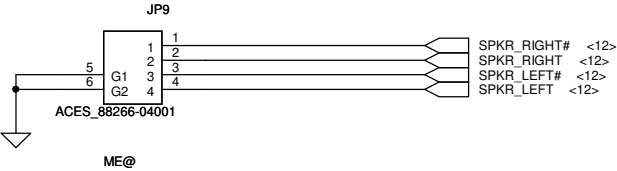
Digital MIC Conn.



AU_VIN0_P	DM_CLK	
AU_VIN0_N	DM_DATAR	

Vibrator

SPAKER



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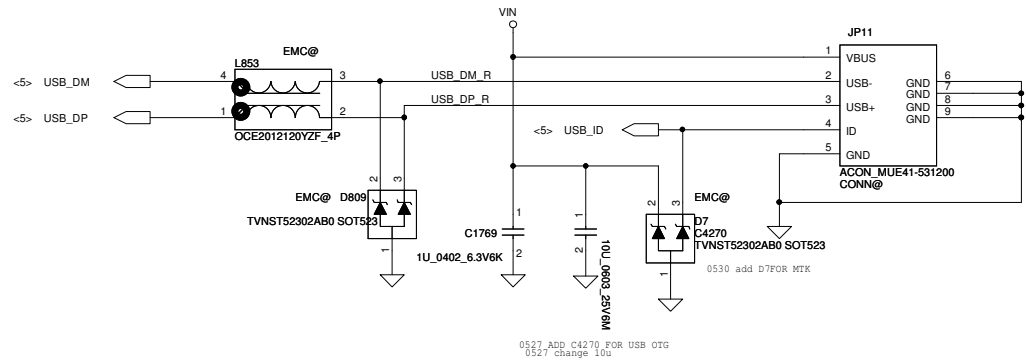
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Size B	Document Number ZEJ00 -LA-A791P		Date: Thursday, August 08, 2013	Sheet 16 of 28

Micro-USB

If mini-A connector insert => CID < 0V => Low
If mini-B connector insert => CID > 1.2V => High
IDPULLUP pin is replaced by 1.2V power source.

FOR RF request

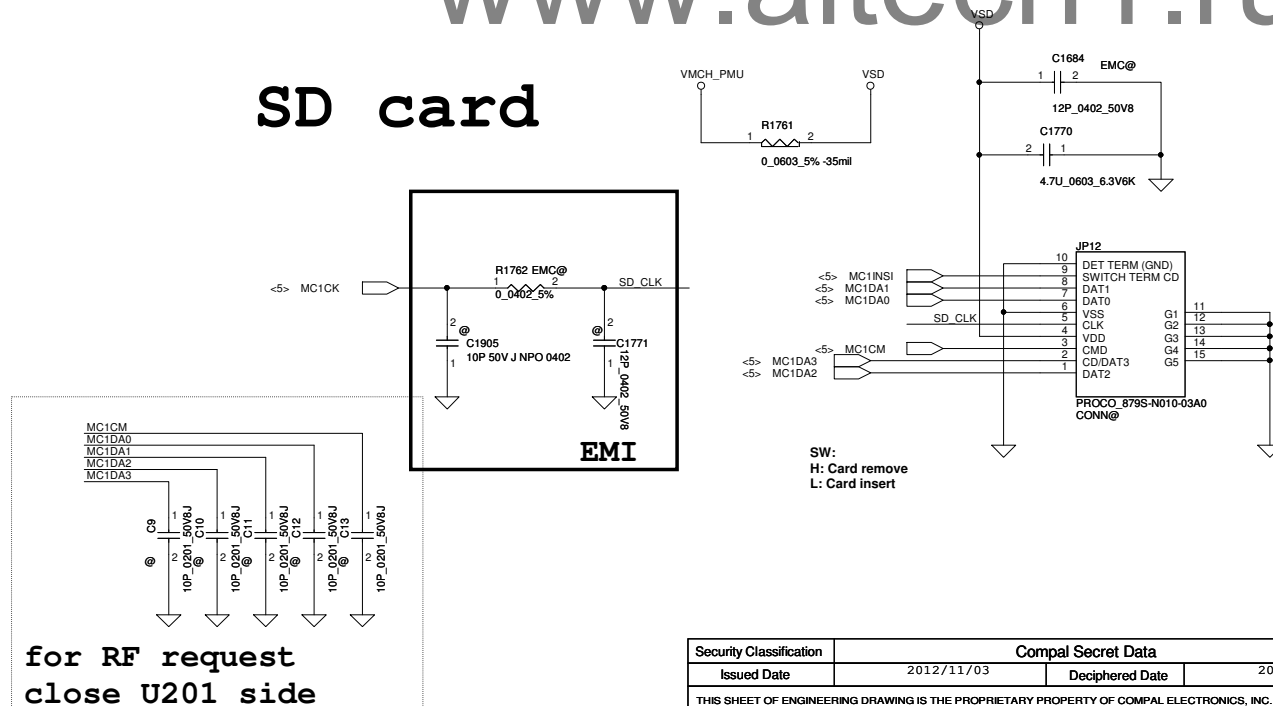
TP1364PAD@ USB_DM_R
TP1365PAD@ USB_DP_R



Compal GND MTK GND

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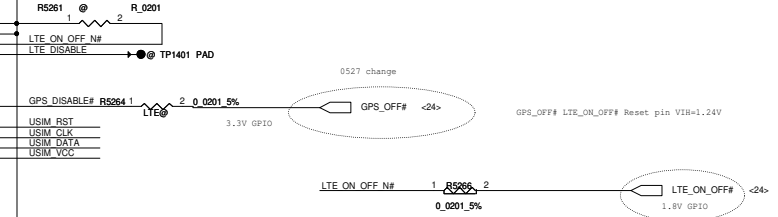
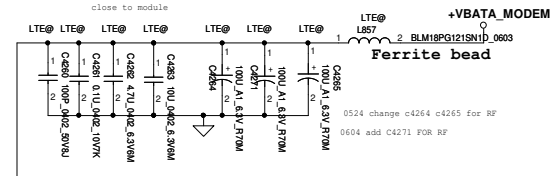
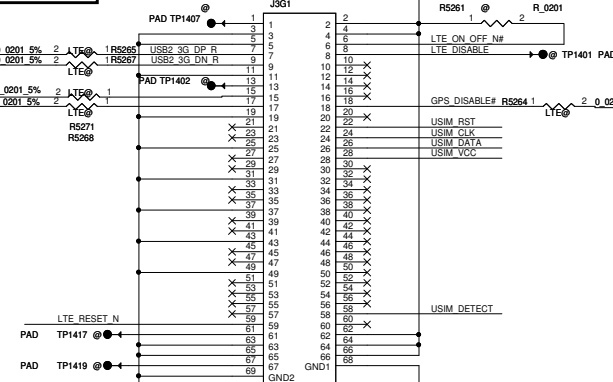
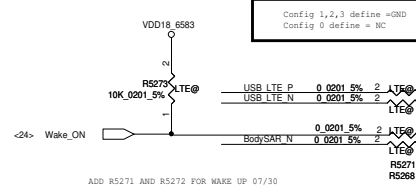
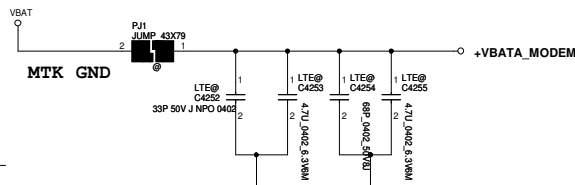
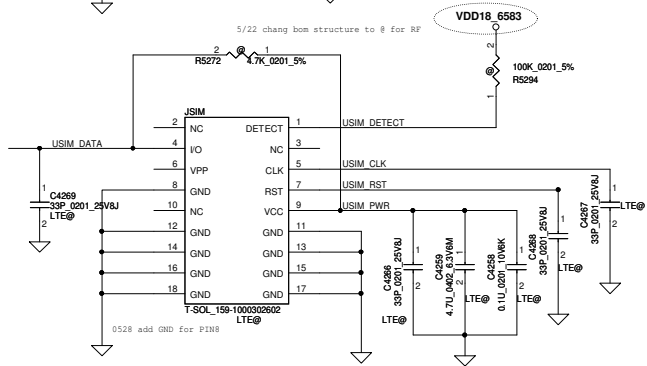
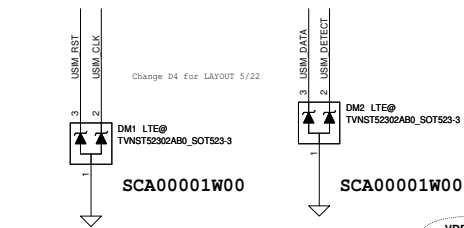
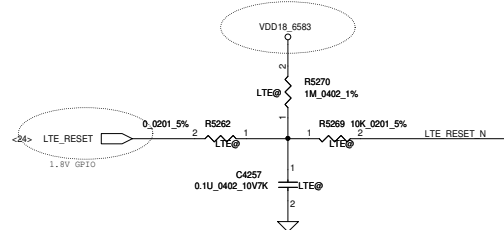
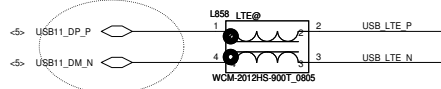
SD card



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				Date Thursday, August 08, 2013	Sheet 17 of 28

3G

Compal GND



USIM_PWRO

2.5V

R6260 260

100k

R6261

1 USIM_VCC

1008 Intel Review update



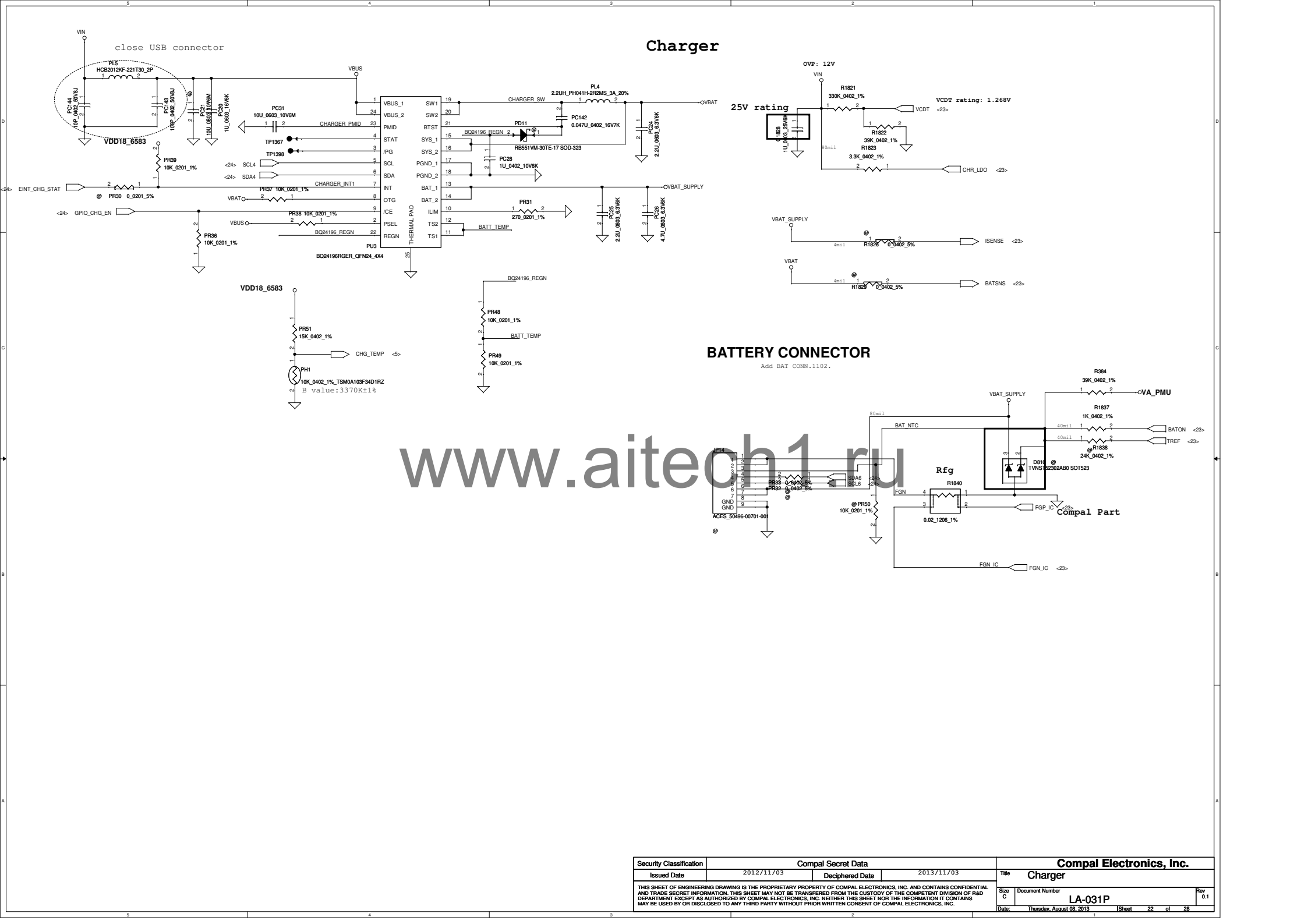
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								Date:				Thursday, August 08, 2013		Sheet	18	of	28

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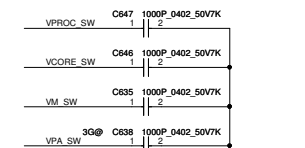
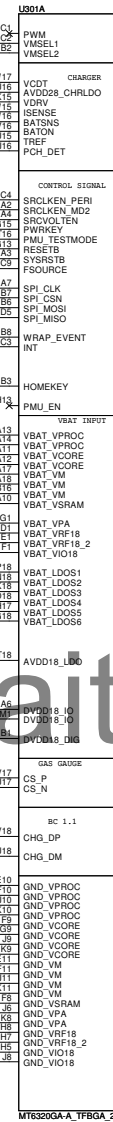
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				Date:	Thursday, August 08, 2013	Sheet 19 of 28

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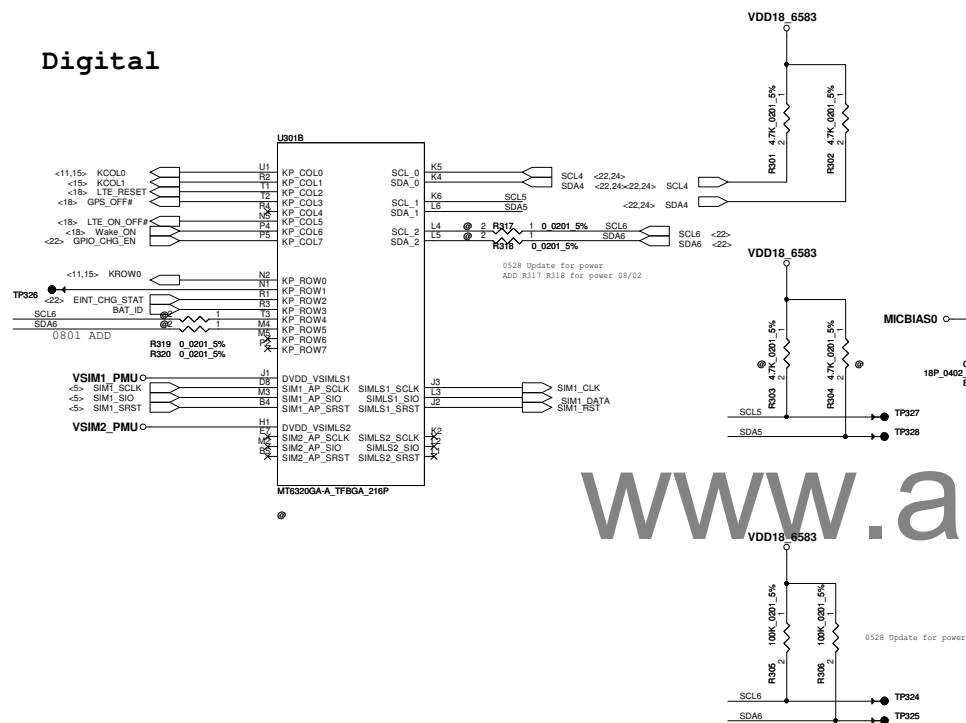


	LPDDR2/1.2V	DDR3U/1.25V	DDR3L/1.35V	DDR3/1.5V
VMSEL2	L	L	H	H
VMSEL1	L	H	L	H

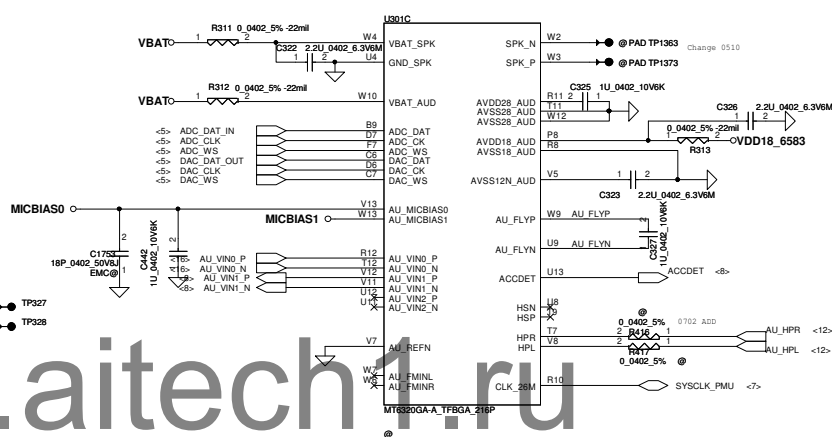


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Date:				Thursday, August 08, 2013	Sheet	23 of 28

Digital



Audio



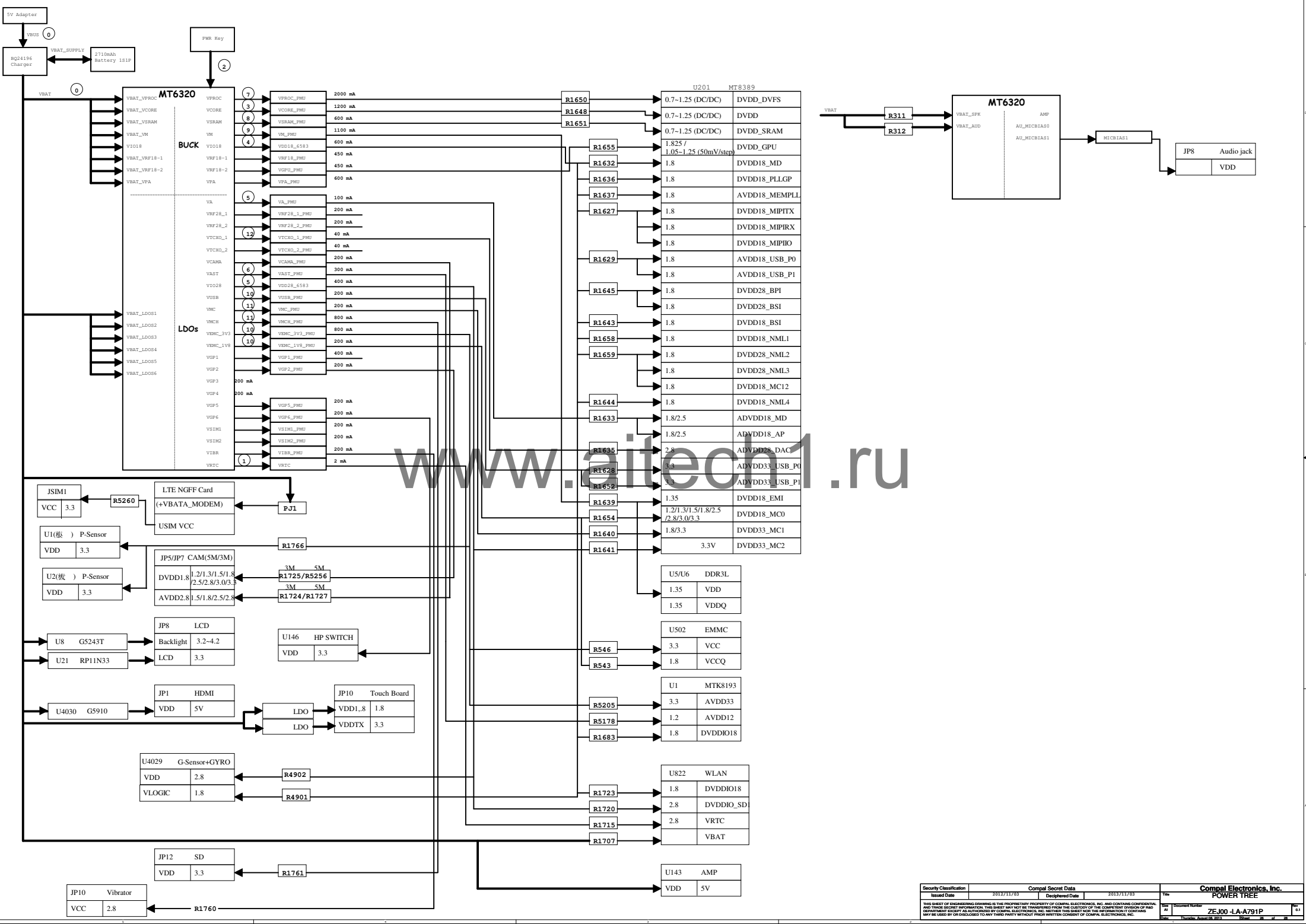
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Date	Thursday, August 28, 2014	Time	1:29 PM	Page 24 of 26

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				Date: Thursday, August 08, 2013	Sheet 25 of 28



DVT-Phase
2013/7/8
Add C1752,C1750 for RF request
change PD11 reserve
change PC31 0805 to 0603 for ME request.

PVT_Phase
2013/8/6
PR30, R339, R321, R322, R323, R324, R325, R326,
R327,R338, R1855, R329 change short pad.
PR31 change 270 ohm. setting input current limit 1.8A

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		Document Number		ZSJVV -LA-A401P	
		Date		Thursday, August 08, 2013	Rev 0.1
				Sheet 27 of 28	

P4 ADD C1757 C1756 C1686 C1687 FOR RF requirement.
P5 ADD C1611 C1754 FOR RF requirement.
P6 DEL Test point for RF -- TP04 TP05 TP07 TP08 TP09 TP10 TP11 TP12 TP1374 TP14 TP15 TP16 TP17 TP24 TP26 TP27 TP28 TP31 TP32 TP33
P7 ADD C1755 FOR RF requirement.
Change U8 /C2650 Package for ME requirement.
P8 ADD C6 for EMI/ESD requirement.
P12 ADD MPU-6515(U4032)/C4177/R4915/R4914 FOR reserve MPU-6515
P13 ADD C1751 FOR RF requirement.
P15 ADD D806 FOR SW2 (ESD requirement.)
P17 ADD C1684 FOR RF requirement.
P23 ADD C1750 C1752 FOR RF requirement.
P24 ADD C1753 FOR RF requirement.

Q1 Q2 QL1 Change BOM from SB000002X00 to SB00000S700
C1890 C4175 Change PN From SE00000V600 to SE00000V680

C TEST

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P5 DEL R1872 ADN R1869
P5 ADD TP1378
P7 Change R5227 BOM Structure to @
P8 ADD C55 FOR EMI ESD
P12 ADD D312 R1269 R1270 C1416 C1420 FOR Audio
P15 Change SW1 SW2 PCB Footprint
P18 ADD R5271 AND R5272 FOR WAKE UP

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				Date Thursday, August 08, 2013	Sheet 28 of 28